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PTO/SB/21 (09-04) (AW 10/2004)

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**TRANSMITTAL
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Total Number of Pages in This Submission 2

Application Number	10/686,974
Filing Date	October 16, 2003
First Named Inventor	Rakesh Batish
Art Unit	2823
Examiner Name	William D. Coleman
Attorney Docket No.	K&S-120US

ENCLOSURES (Check all that apply)

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Remarks:**SIGNATURE OF APPLICANT, ATTORNEY OR AGENT**

Firm Name RatnerPrestia

Signature

Printed Name Jacques L. Etkowicz

Date December 9, 2004

Registration No.

41,738

CERTIFICATE OF TRANSMISSION / MAILING

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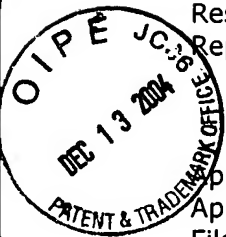
December 9, 2004

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, ALEXANDRIA, VA 22313-1450.

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Appln. No.: 10/686,974
Response Dated December 9, 2004
Reply to Office Action of November 24, 2004

K&S-120US



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No.: 10/686,974
Applicants: Rakesh Batish et al.
Filed: October 16, 2003
Title: SYSTEM AND METHOD FOR REDUCING OR ELIMINATING
SEMICONDUCTOR DEVICE WIRE SWEEP
TC/A.U.: 2823
Examiner: William D. Coleman
Confirmation No.: 8097
Docket No.: K&S-120US

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S I R :

This is in response to the Restriction Requirement stated in the Office Action dated **November 24, 2004**.

The Examiner requires that claims of either Group I - Claims 1-11 or Group II - Claims 12-23 be elected for prosecution. Applicants elect to prosecute Group I - Claims 1-11, drawn to a method of manufacturing a semiconductor device, classified in class 438, subclass 112. This election is made without traverse.

Respectfully submitted,

RatnerPrestia



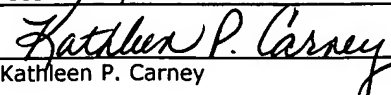
Jacques L. Etkowicz, Reg. No. 41,738
Attorney for Applicants

JLE/kpc
Dated: December 9, 2004
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Kathleen P. Carney